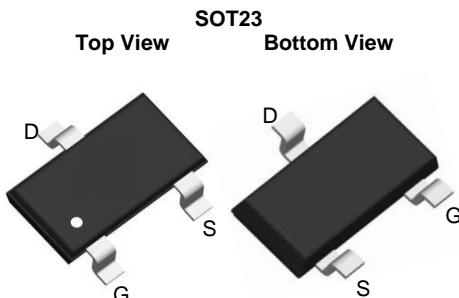


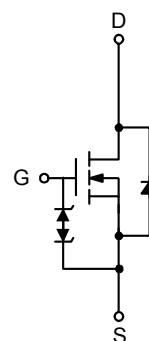
## N-Channel Enhancement Mode MOSFET

- 20V/6A
- $R_{DS(ON)}=20m\Omega$  (typ) @ $VGS=4.5V$   
 $R_{DS(ON)}=24m\Omega$  (typ) @ $VGS=2.5V$   
 $R_{DS(ON)}=30m\Omega$  (typ) @ $VGS=1.8V$
- 100% UIS & RG Tested
- Reliable and Rugged
- Lead Free and Green Devices Available  
(RoHS Compliant)



## Applications

- Power Management for Industrial DC/DC Converters



N-Channel MOSFET

## Absolute Maximum Ratings ( $T_A = 25^\circ C$ unless otherwise noted)

| Symbol                | Parameter                              | Rating           | Unit |
|-----------------------|--|------------------|------|
| <b>Common Ratings</b> |  |                  |      |
| $V_{DSS}$             | Drain-Source Voltage                   | 20               | V    |
| $V_{GSS}$             | Gate-Source Voltage                    | $\pm 12$         |      |
| $I_D$                 | Continuous Drain Current               | 6                | A    |
| $I_{DM}$              | Pulsed Drain Current                   | 20               |      |
| $I_S$                 | Diode Continuous Forward Current       | 1                | A    |
| $T_{STG}, T_j$        | Storage Temperature Range              | -55 to 150       | °C   |
| PD                    | Power Dissipation                      | $T_A=25^\circ C$ | W    |
|                       |  | $T_A=70^\circ C$ |      |
| $R_{\theta JA}$       | Thermal Resistance-Junction to Ambient | 62.5             | °C/W |

**Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.**

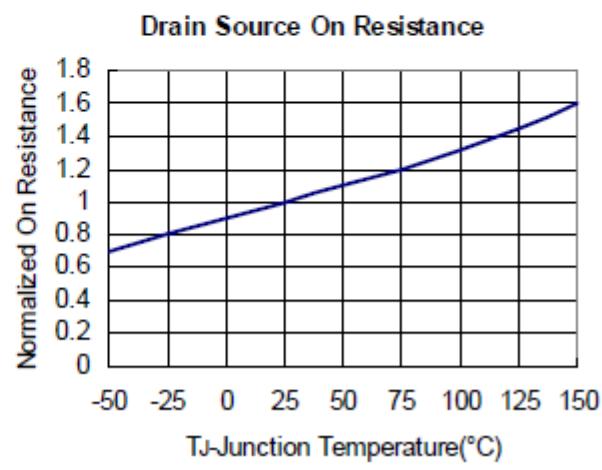
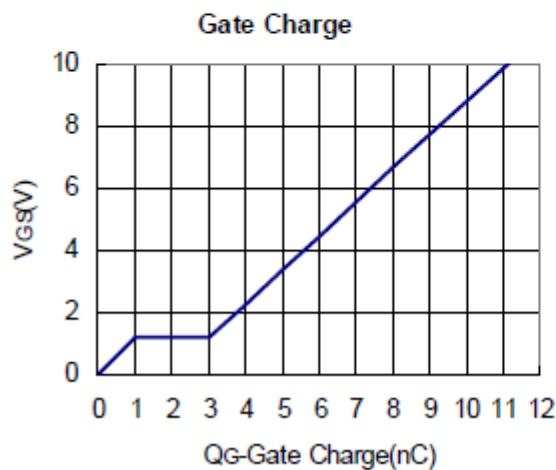
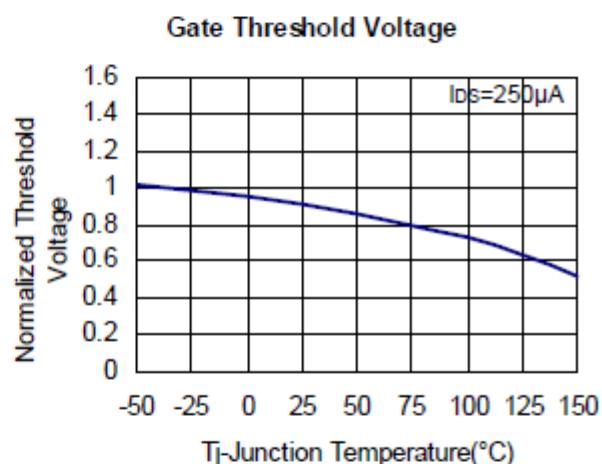
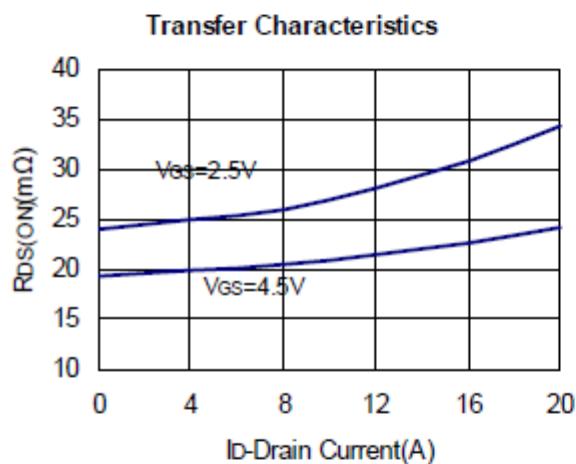
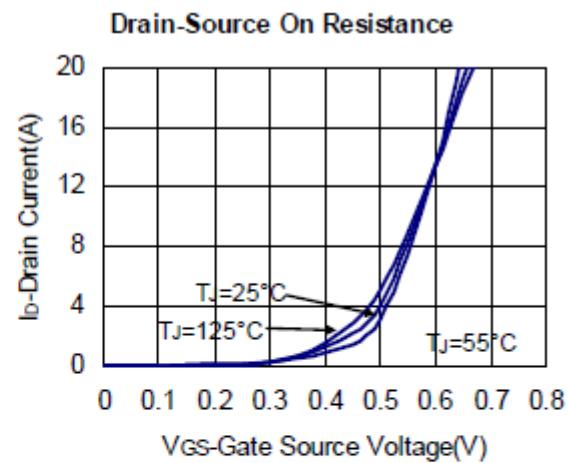
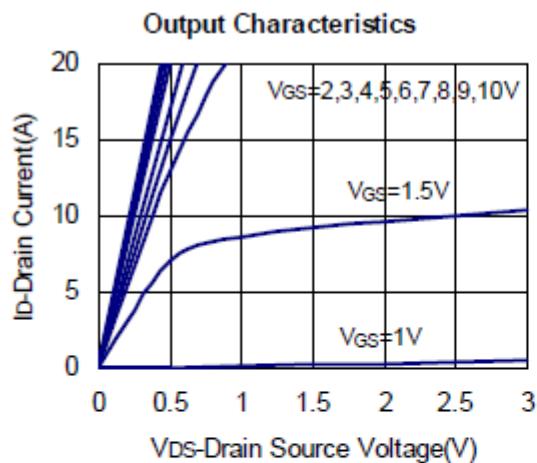
**Absolute maximum ratings are stress rating only and functional device operation is not implied**

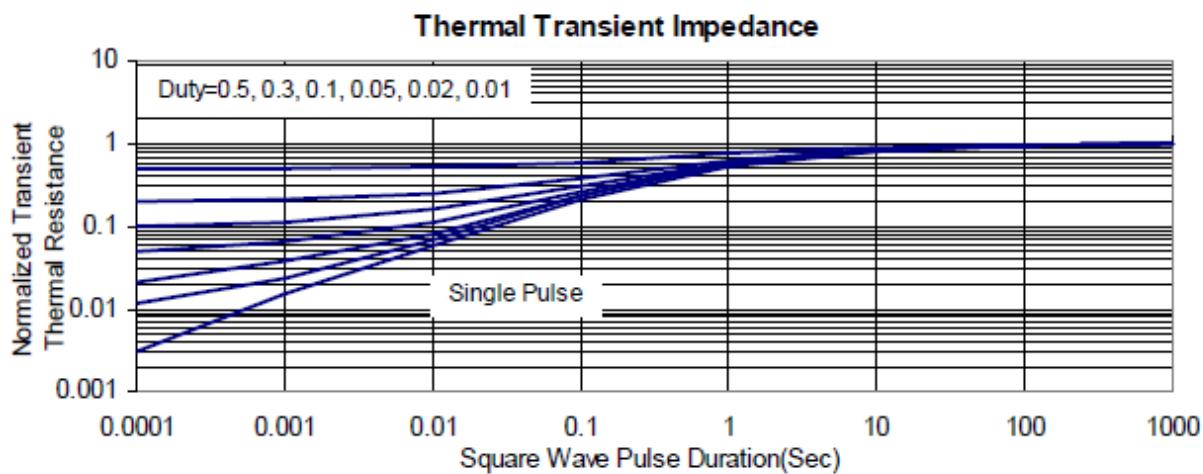
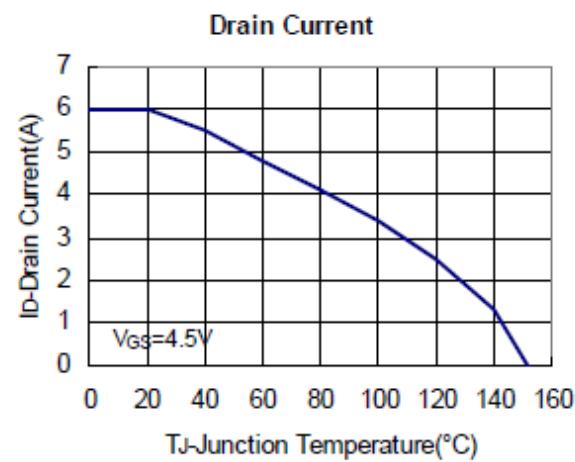
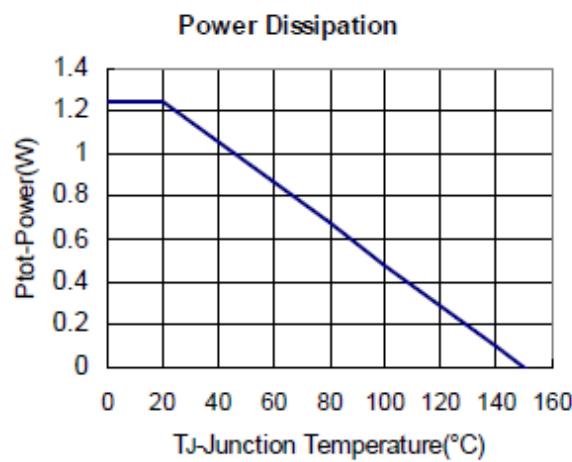
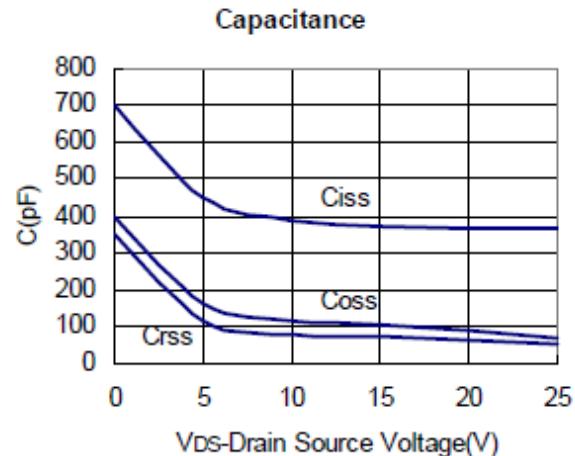
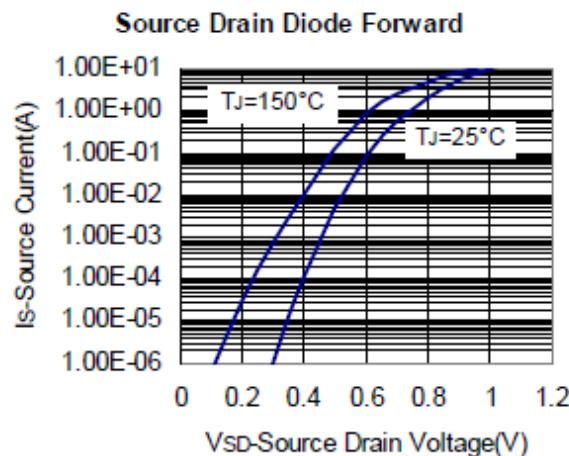
## Electrical Characteristics ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

| Symbol                             | Parameter                        | Test Conditions  | Min. | Typ. | Max.      | Unit             |
|------------------------------------|----------------------------------|--|------|------|-----------|------------------|
| <b>Static Characteristics</b>      |                                  |  |      |      |           |                  |
| $BV_{DSS}$                         | Drain-Source Breakdown Voltage   | $V_{GS}=0\text{V}, I_{DS}=250\mu\text{A}$  | 20   | -    | -         | V                |
| $I_{DSS}$                          | Zero Gate Voltage Drain Current  | $V_{DS}=16\text{V}, V_{GS}=0\text{V}$  | -    | -    | 1         | $\mu\text{A}$    |
|                                    |                                  | $T_J=85^\circ\text{C}$   | -    | -    | 30        |                  |
| $V_{GS(th)}$                       | Gate Threshold Voltage           | $V_{DS}=V_{GS}, I_{DS}=250\mu\text{A}$   | 0.5  | -    | 1         | V                |
| $I_{GSS}$                          | Gate Leakage Current             | $V_{GS}=\pm 12\text{V}, V_{DS}=0\text{V}$  | -    | -    | $\pm 100$ | nA               |
| $R_{DS(ON)}$                       | Drain-Source On-state Resistance | $V_{GS}=4.5\text{V}, I_{DS}=6\text{A}$   | -    | 20   | 30        | $\text{m}\Omega$ |
|                                    |                                  | $V_{GS}=2.5\text{V}, I_{DS}=5\text{A}$   | -    | 24   | 35        |                  |
|                                    |                                  | $V_{GS}=1.8\text{V}, I_{DS}=4\text{A}$   | -    | 30   | 40        |                  |
| $G_{fs}$                           | Forward Transconductance         | $V_{DS}=5\text{V}, I_D=3.6\text{A}$  | -    | 10   | -         | S                |
| <b>Body Diode Characteristics</b>  |                                  |  |      |      |           |                  |
| $V_{SD}$                           | Diode Forward Voltage            | $I_{SD}=1.7\text{A}, V_{GS}=0\text{V}$   | -    | 0.8  | 1.3       | V                |
| <b>Dynamic Characteristics</b>     |                                  |  |      |      |           |                  |
| $C_{iss}$                          | Input Capacitance                | $V_{GS}=0\text{V}, V_{DS}=10\text{V},$<br>$\text{Frequency}=1.0\text{MHz}$             | -    | 595  | -         | $\text{pF}$      |
| $C_{oss}$                          | Output Capacitance               |  | -    | 140  | -         |                  |
| $C_{rss}$                          | Reverse transfer capacitance     |  | -    | 125  | -         |                  |
| $t_{d(ON)}$                        | Turn-on delay Time               | $V_{GS}=4.5\text{V}, V_{DS}=10\text{V}$<br>$R_G=6\Omega, I_D=1\text{A}, R_L=10\Omega,$ | -    | 3.6  | 7         | $\text{nS}$      |
| $t_r$                              | Turn-on rise Time                |  | -    | 13.5 | 25        |                  |
| $t_{d(OFF)}$                       | Turn-off delay Time              |  | -    | 32   | 58        |                  |
| $t_f$                              | Turn-off rise Time               |  | -    | 6.6  | 13        |                  |
| <b>Gate Charge Characteristics</b> |                                  |  |      |      |           |                  |
| $Q_g$                              | Total Gate Charge                | $V_{DS}=10\text{V}, V_{GS}=4.5\text{V},$<br>$I_{DS}=6\text{A}$                         | -    | 21   | -         | $\text{nC}$      |
| $Q_{gs}$                           | Gate-Source Charge               |  | -    | 1.3  | -         |                  |
| $Q_{gd}$                           | Gate-Drain Charge                |  | -    | 3.3  | -         |                  |

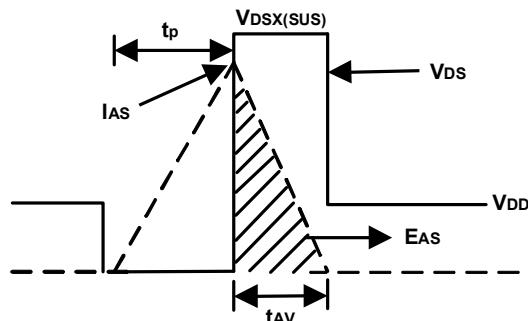
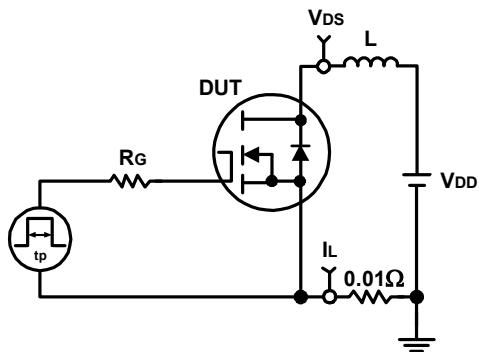
**Note: 1. Pulse test: pulse width<=300uS, duty cycle<=2%**

**2. Static parameters are based on package level with recommended wire bonding**

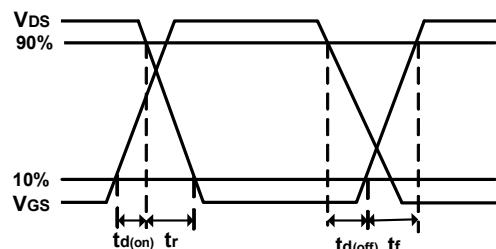
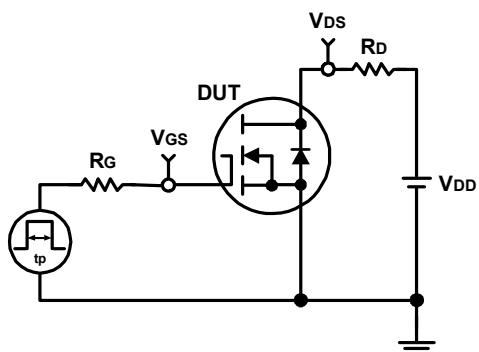
**TYPICAL CHARACTERISTICS (25°C Unless Note)**


**TYPICAL CHARACTERISTICS (continuous)**


## Avalanche Test Circuit and Waveforms

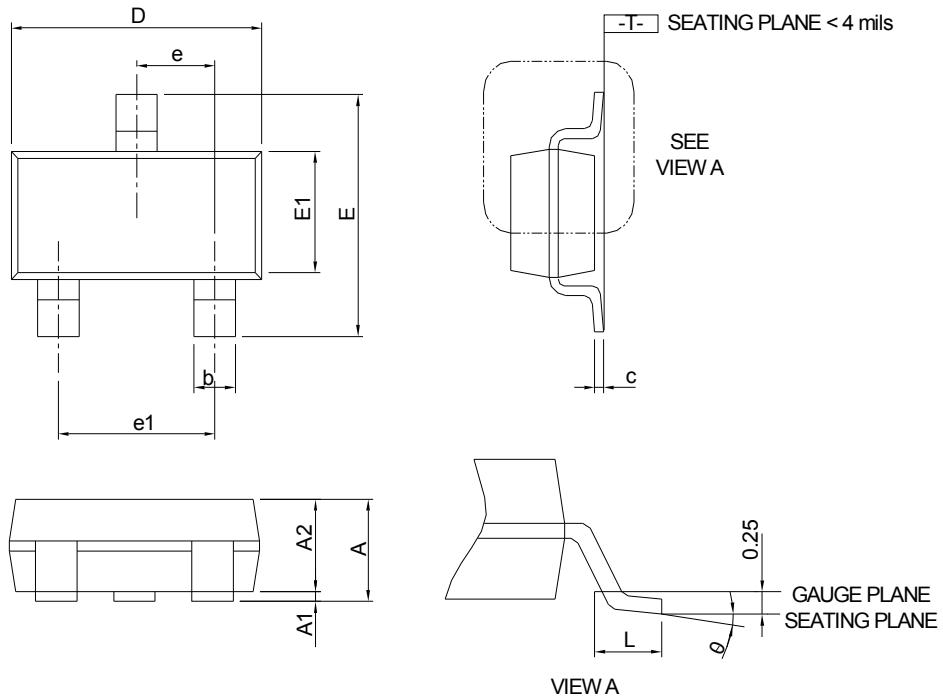


## Switching Time Test Circuit and Waveforms



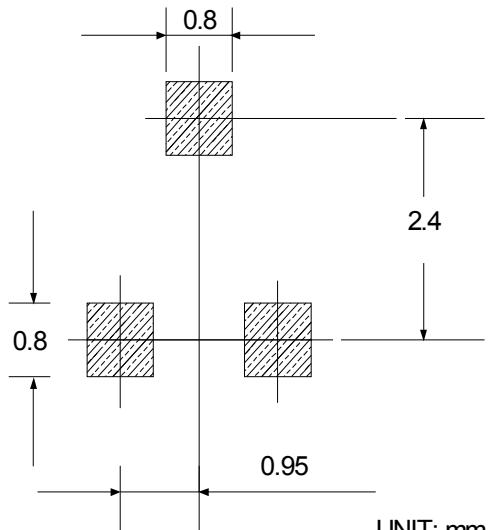
## Package Information

SOT23-3L



| SYMBOL   | SOT23-3L    |      |           |       |
|----------|-------------|------|-----------|-------|
|          | MILLIMETERS |      | INCHES    |       |
|          | MIN.        | MAX. | MIN.      | MAX.  |
| A        |             | 1.20 |           | 0.047 |
| A1       | 0.00        | 0.08 | 0.000     | 0.003 |
| A2       | 0.90        | 1.12 | 0.035     | 0.044 |
| b        | 0.30        | 0.50 | 0.012     | 0.020 |
| c        | 0.08        | 0.22 | 0.003     | 0.009 |
| D        | 2.70        | 3.10 | 0.106     | 0.122 |
| E        | 2.60        | 3.00 | 0.102     | 0.118 |
| E1       | 1.40        | 1.80 | 0.055     | 0.071 |
| e        | 0.95 BSC    |      | 0.037 BSC |       |
| e1       | 1.90 BSC    |      | 0.075 BSC |       |
| L        | 0.30        | 0.60 | 0.012     | 0.024 |
| $\theta$ | 0°          | 8°   | 0°        | 8°    |

### RECOMMENDED LAND PATTERN



Note : Dimension D and E1 do not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.